

COMPRESSED SERIAL BUS

Abstract

There is provided a serial compressed bus interface
5 having a reduced pin count. The interface includes a
serial-to-parallel converter having a single serial data
input line adapted to receive time-division multiplexed
serial data from a plurality of data sources. Enable logic
is adapted to input at least one data valid signal that
10 identifies each of a plurality of data consumers for which
the time-division multiplexed serial data is valid.

09276244-1-2000